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	Substitut	e for form 1449/	VPTO		Complete if Known			
					Application Number	10/743,307		
	INFO	<b>RMATION</b>	DISCLOS	URE	Filing Date	December 23, 2003		
•	STAT	EMENT B	Y APPLIC	ANT	First Named Inventor	ZHANG, QI		
					Art Unit	-2183- 2/93		
	(us	e as many shee	ts as necessar	y)		Net yet known Charles J.		
Sheet	t	1	of	2	Attorney Docket Number	P-6215-US		

U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite, No. 1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant		
		· Number-Kind Code <sup>2 (f known)</sup>			Figures Appear		
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Initials*	Cite, No. 1	Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	MIVEOU-TTTT	Applicant of Cited Document	Where Relevant Pesseges or Relevant Figures Appear		
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Subs	titute for form 144	9B/PTO		Complete if Known			
				Application Number	10/743,307		
INF	INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Filing Date	December 23, 2003		
STA				First Named Inventor	ZHANG, Qi		
				Art Unit	<del>2183</del> 2193.		
(use as many sheets as necessary)				Examiner Name	Not yet known Chavis J.		
Sheet	2	of	2	Attorney Docket Number	P-6215-US		

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9	D	ANONYMOUS: "Rounding-mode independent implementation of float-to-integer truncation", Research Disclosure, April 2000,vol. 432, no. 58, Mason Publications, Hampshire, GB.	
<b>P</b>	E	INTEL CORPORATION: "Intel Architecture Optimization-Reference Manual"[Online], February 1999, pp.2-25, 2-27, 5-42. Retrieved from www.intel.co.jp/design/pentiumi/i/manuals/24512701.pdf	
(g)	F	International Search Report dated March 30, 2006 for Application No. PCT/US2004/041849.	

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